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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10016540	FILING DATE 10/26/2001	CLASS 370	SUBCLASS 710	GAU 2564	EXAMINER Gulha
**APPLICANTS: Boxer Lawrence; Castagnozzi Dan; 2162					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/245,895 11/03/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO AMCC-001XX	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)	
Verified and Acknowledged Examiners initials					
TITLE : Parallel data bus with bit position encoded on the clock wire					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.	Print Fig.
		Application Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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